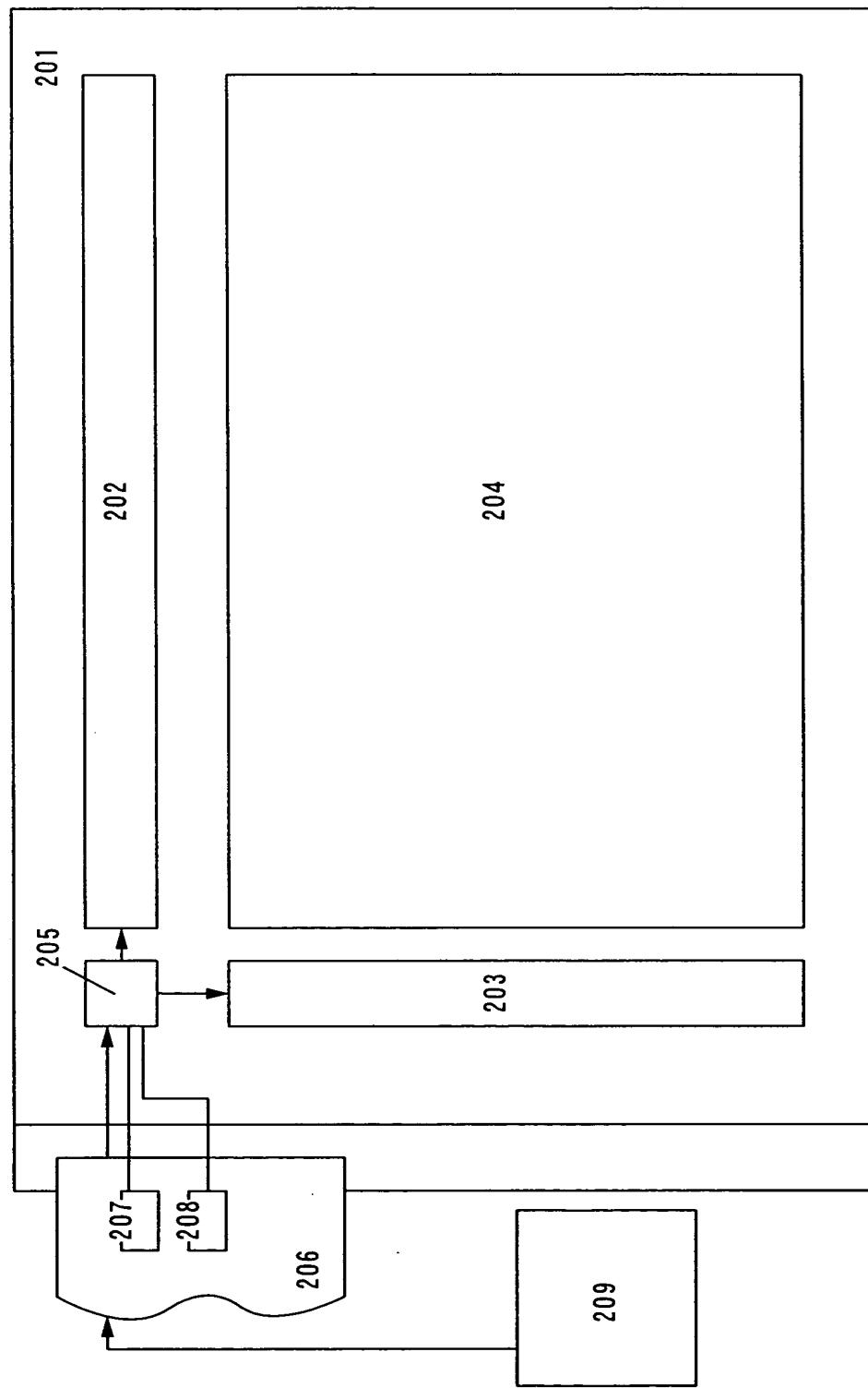


Fig. 1

**Fig. 2**



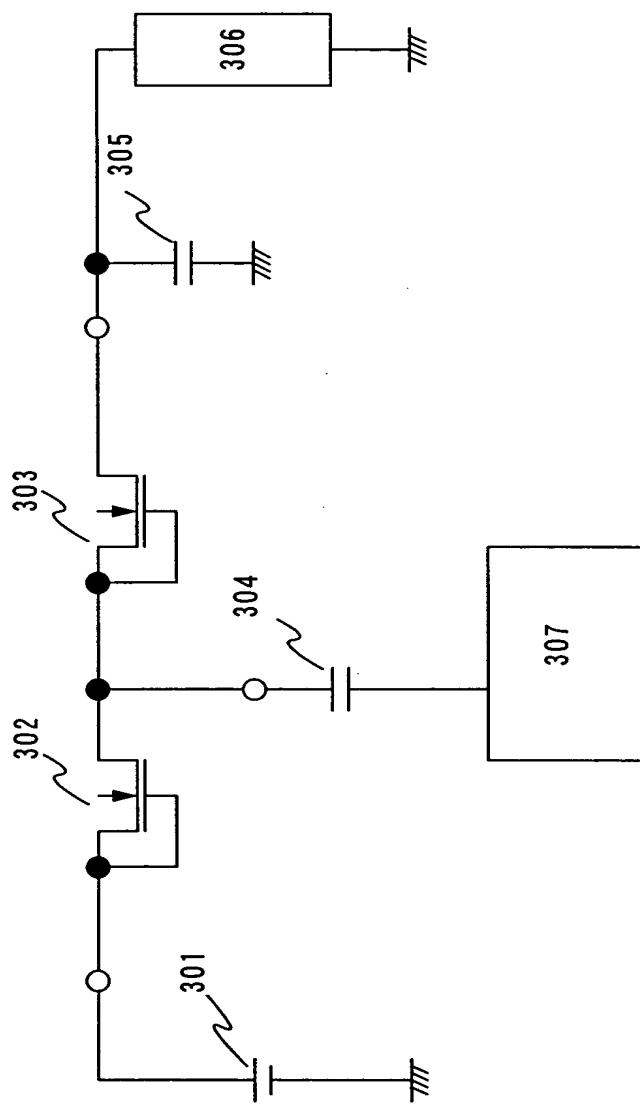
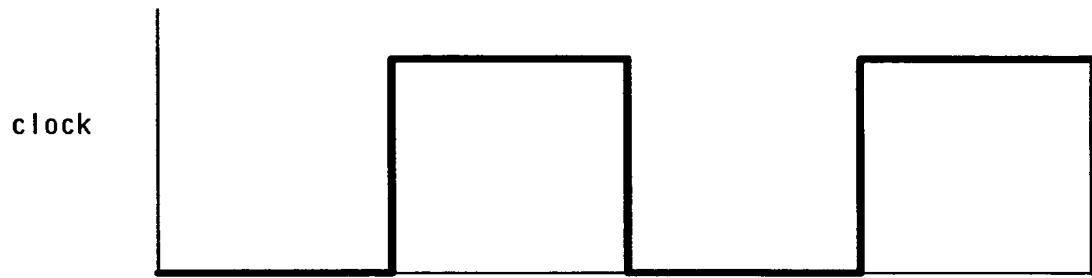
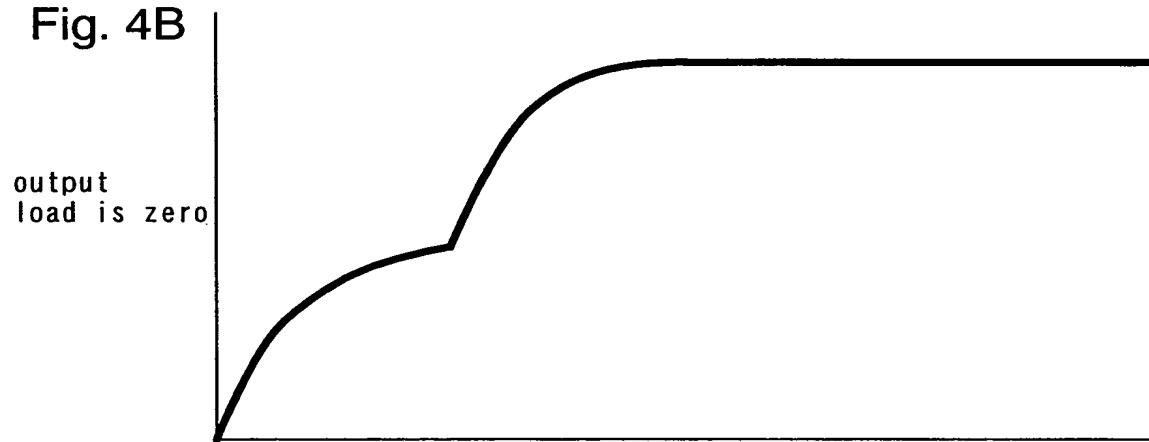


Fig. 3

**Fig. 4A**

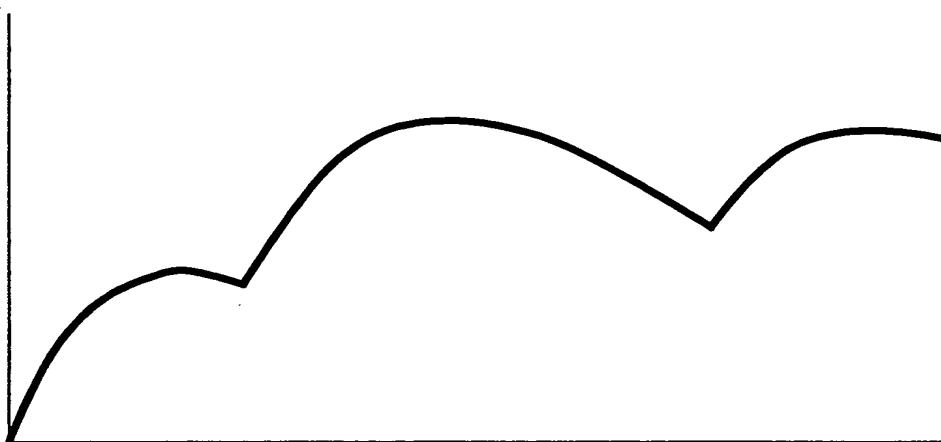


**Fig. 4B**

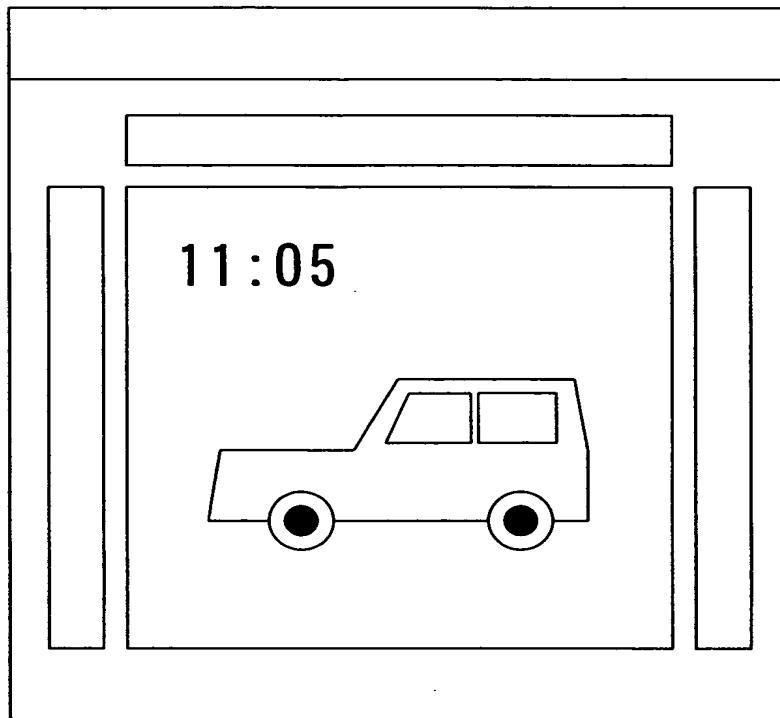


**Fig. 4C**

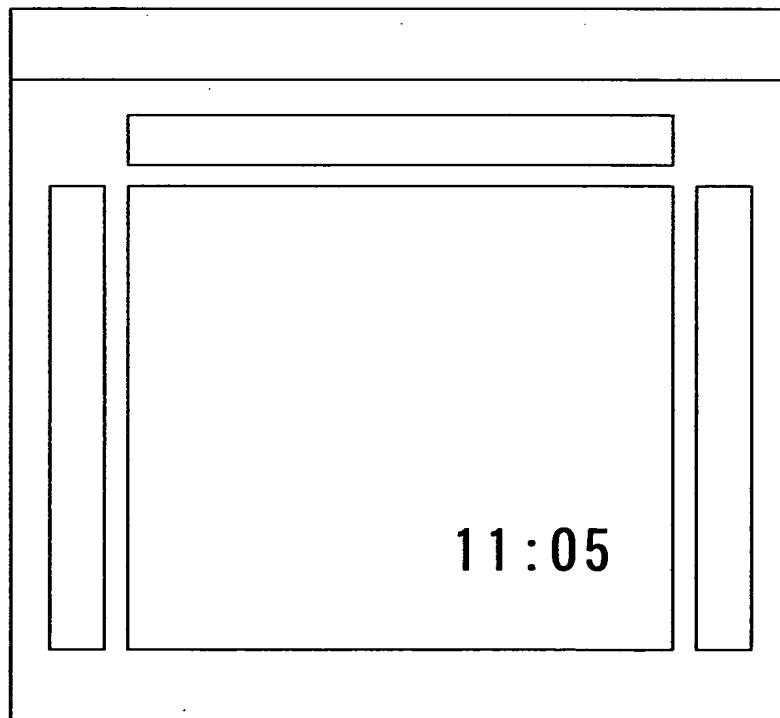
output  
load is non-zero



**Fig. 5A**



**Fig. 5B**



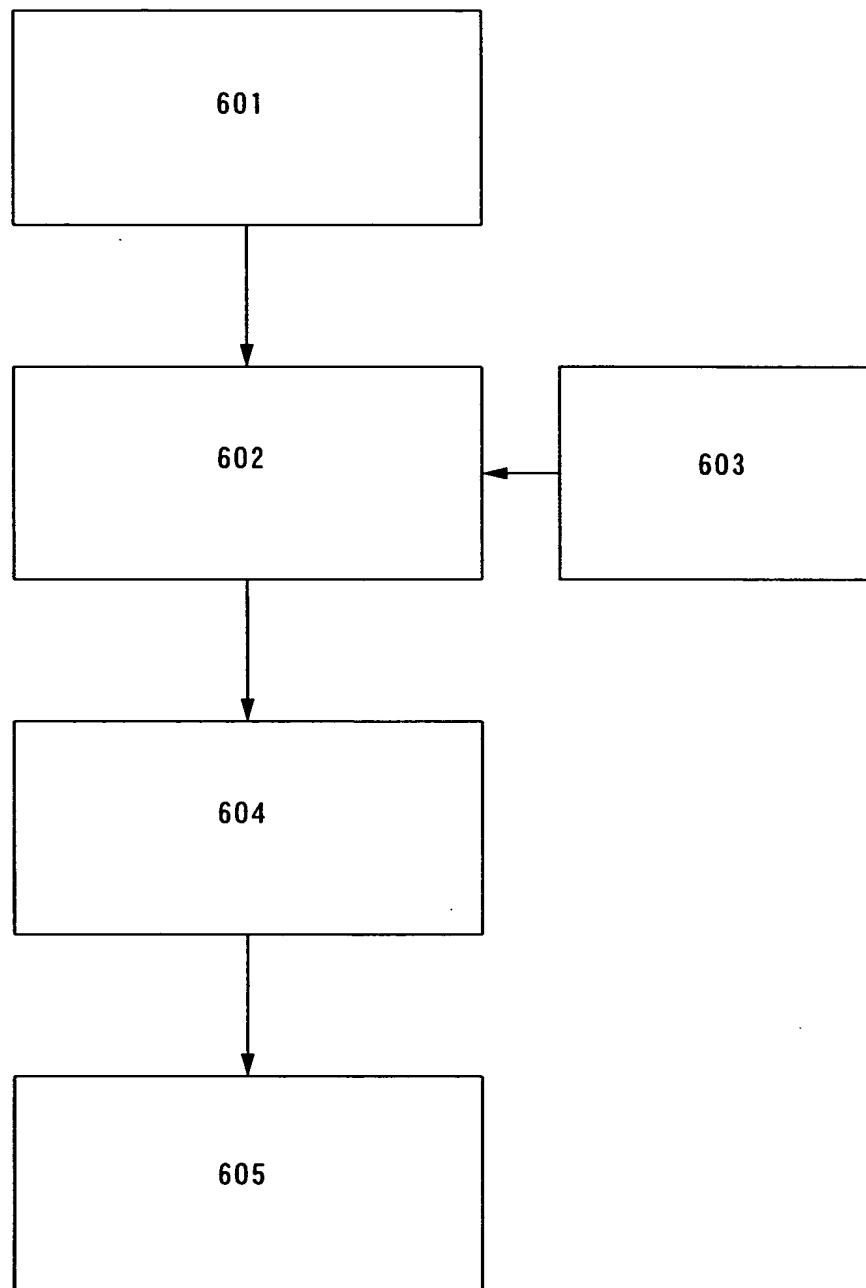


Fig. 6

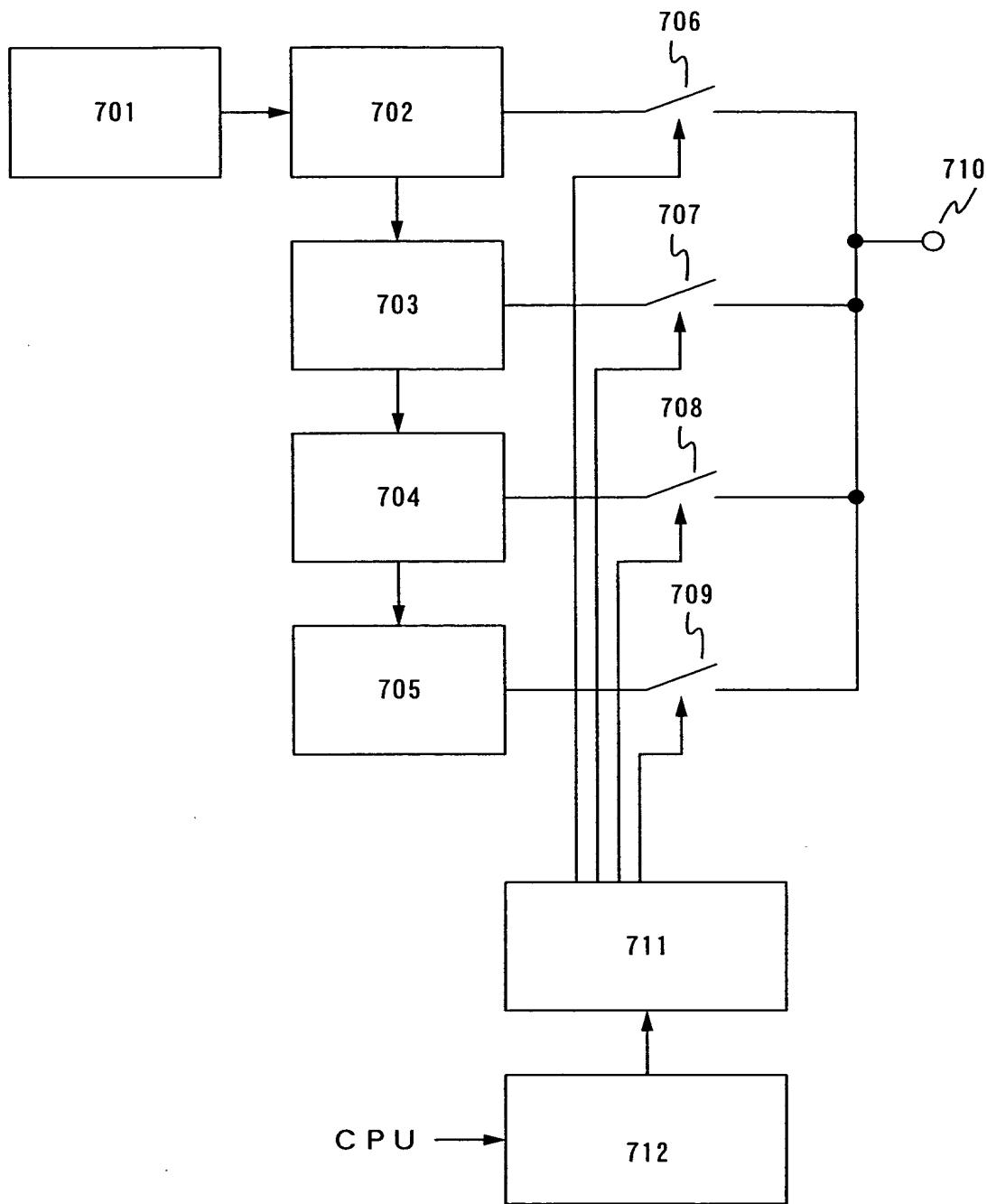


Fig. 7

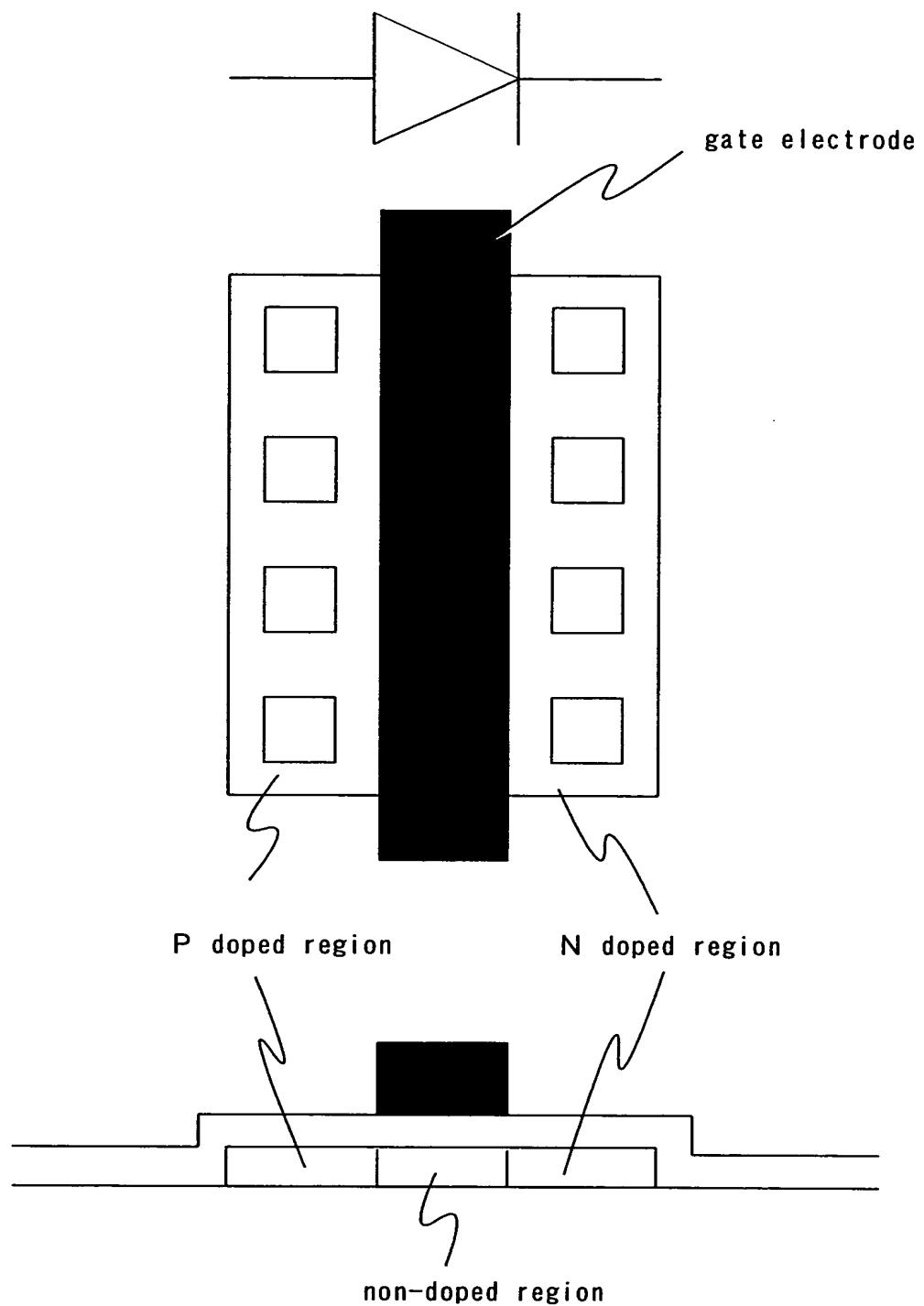


Fig. 8

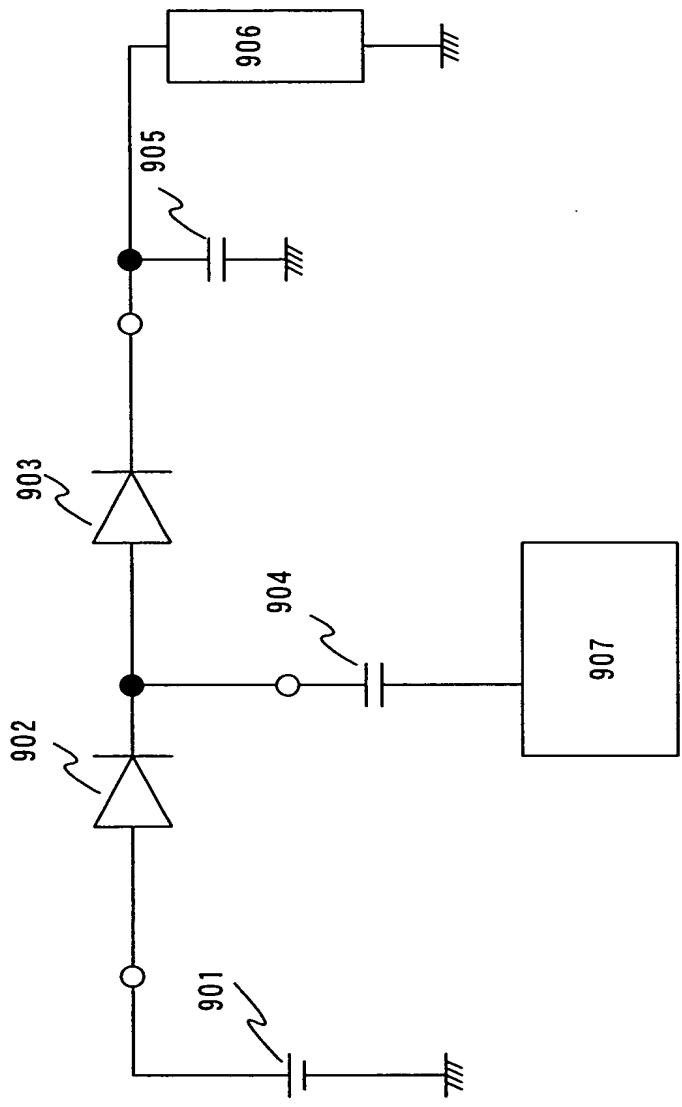


Fig. 9

